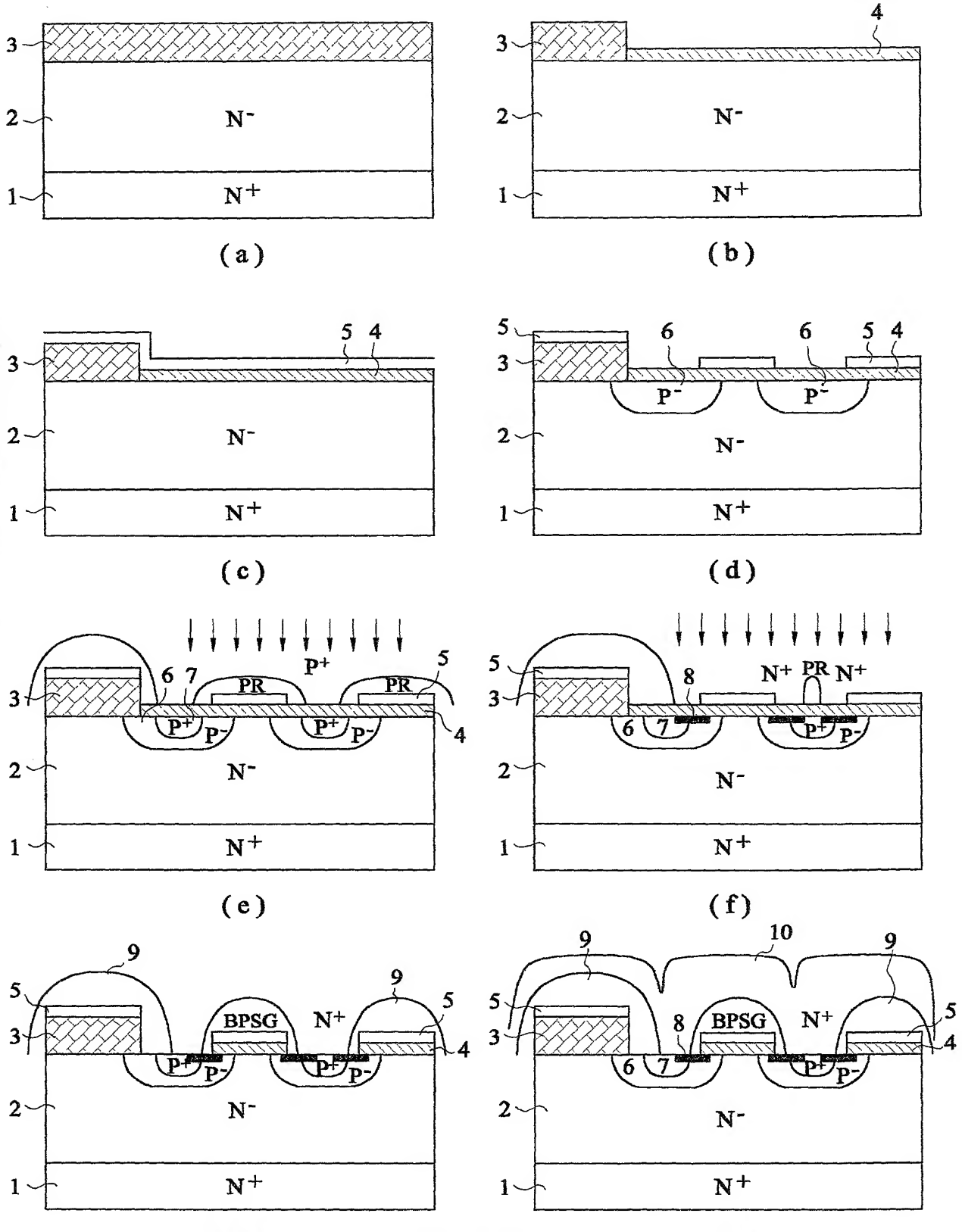


(PRIOR ART)

FIG. 1

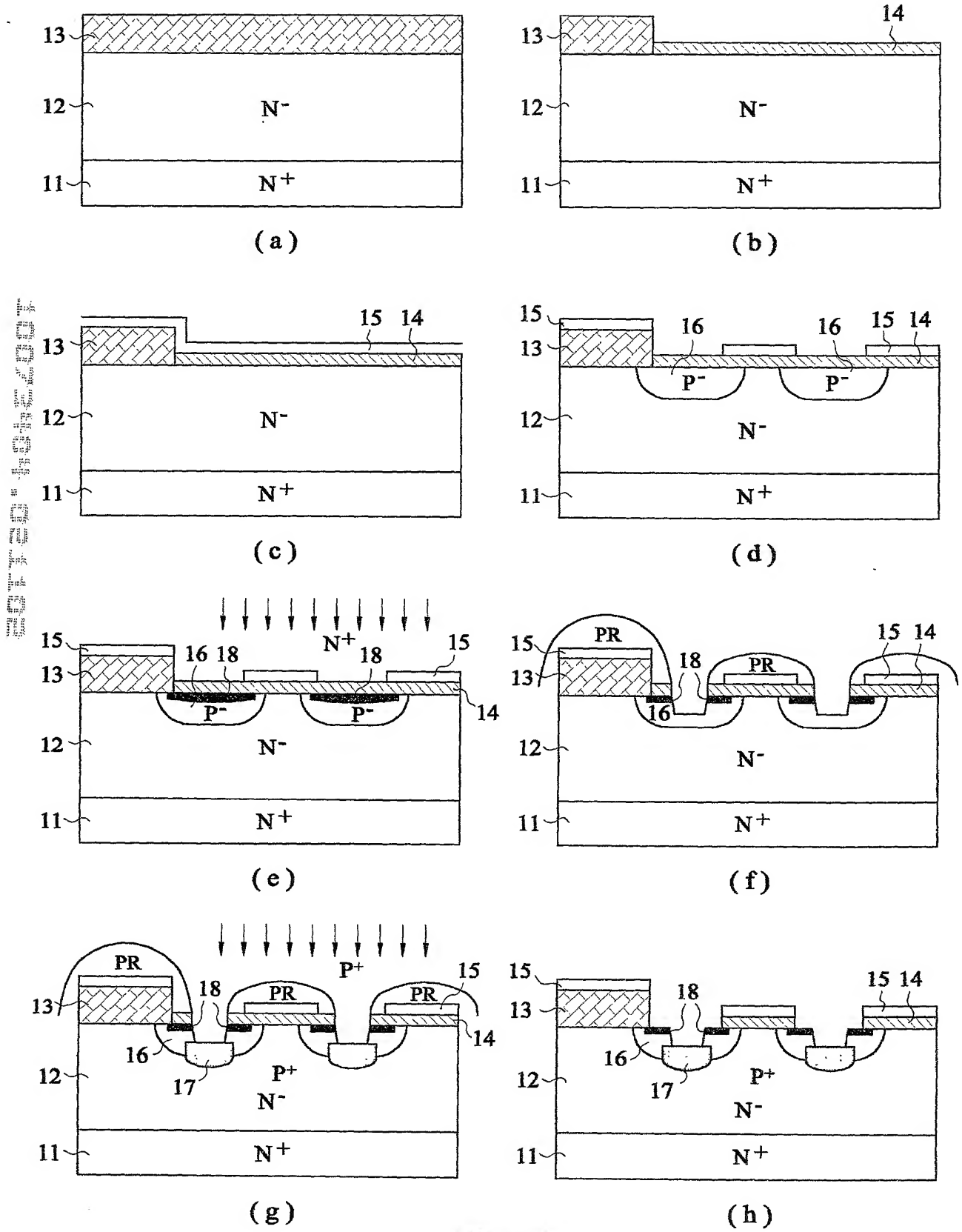


FIG. 2

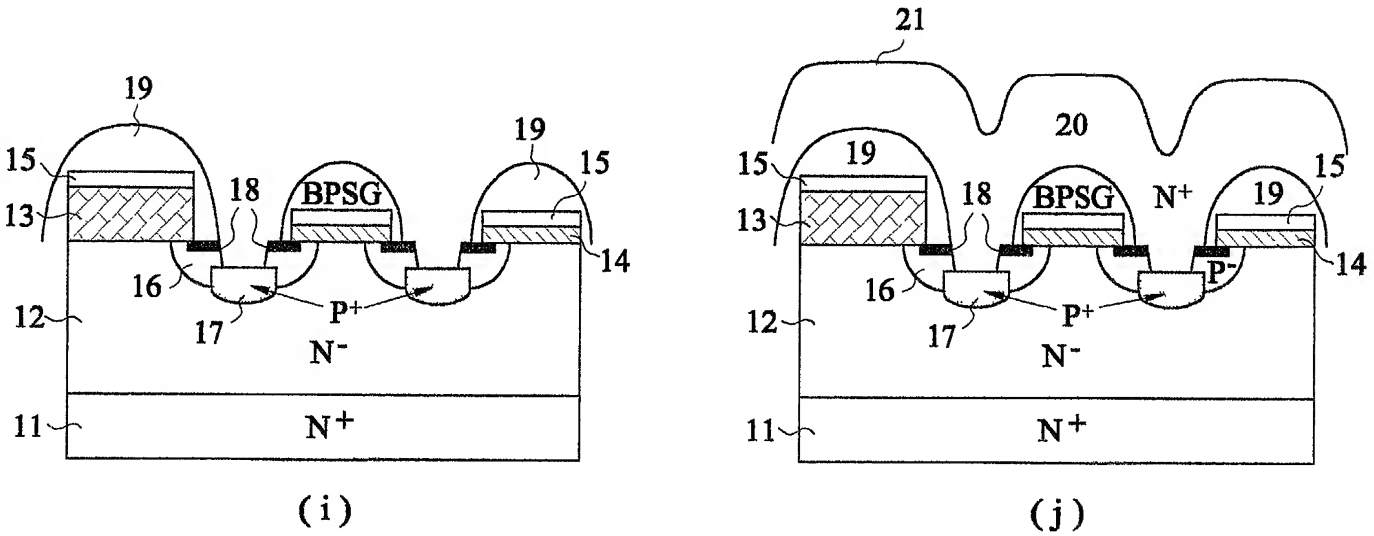
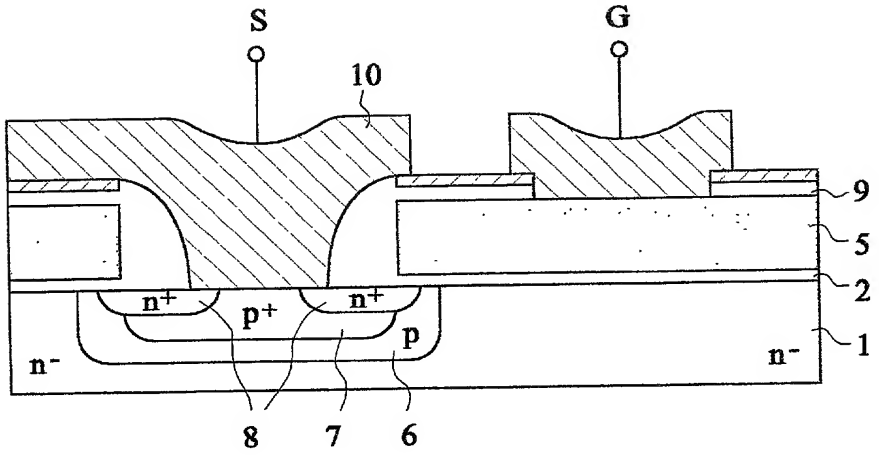
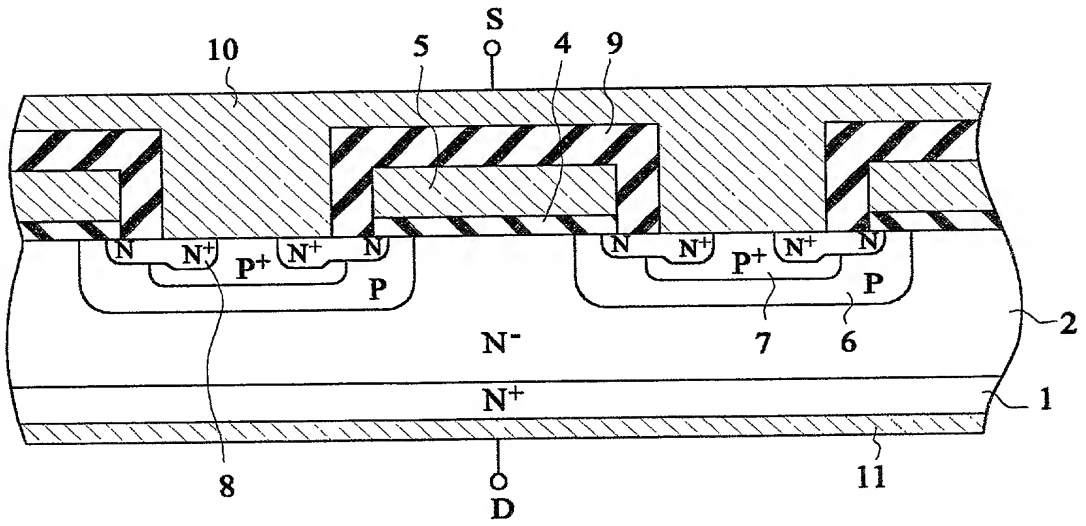

19
15
13
18
BPSG
19
15
14
16
17
P+
12
N-
11
N+
(i)
21
20
15
19
13
18
BPSG
N+
19
15
14
16
17
P+
P-
12
N-
11
N+
(j)

FIG. 2



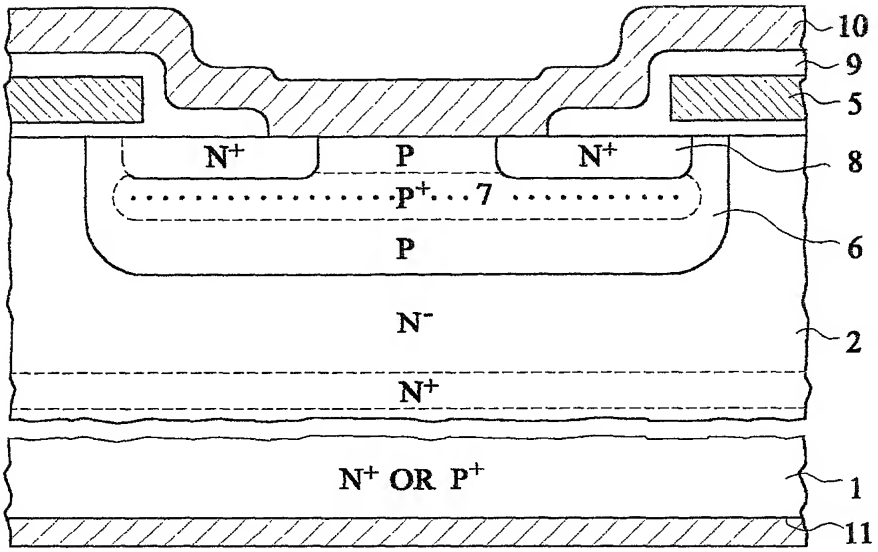
(PRIOR ART)

FIG. 3



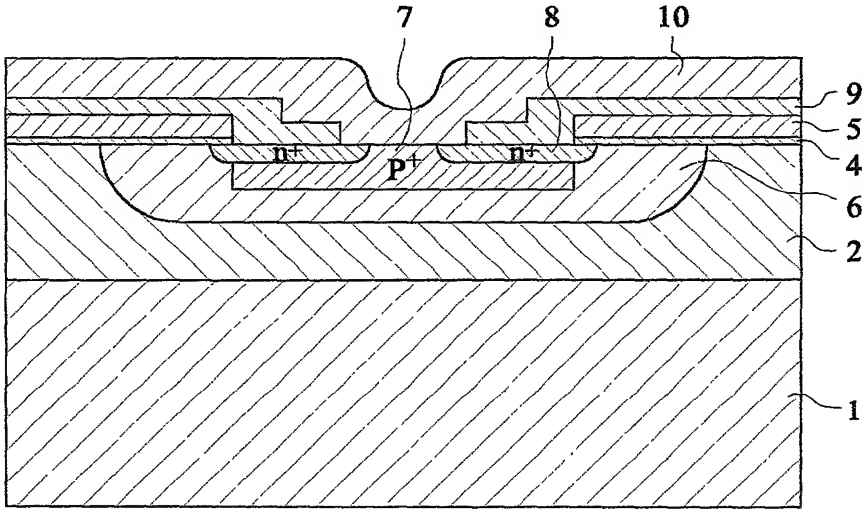
(PRIOR ART)

FIG. 4



(PRIOR ART)

FIG. 5



(PRIOR ART)

FIG. 6

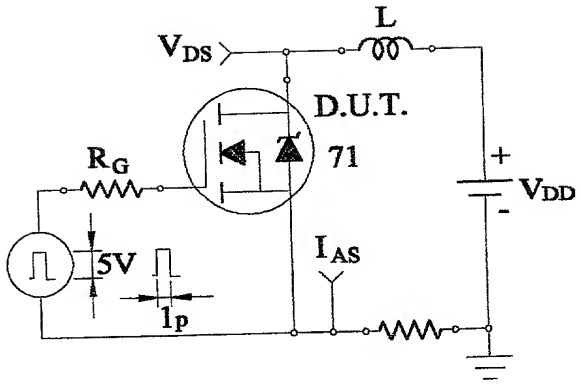


FIG. 7a

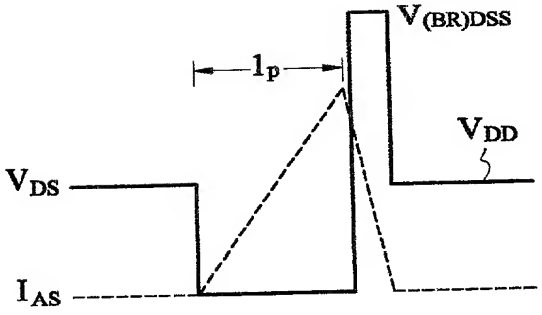


FIG. 7b

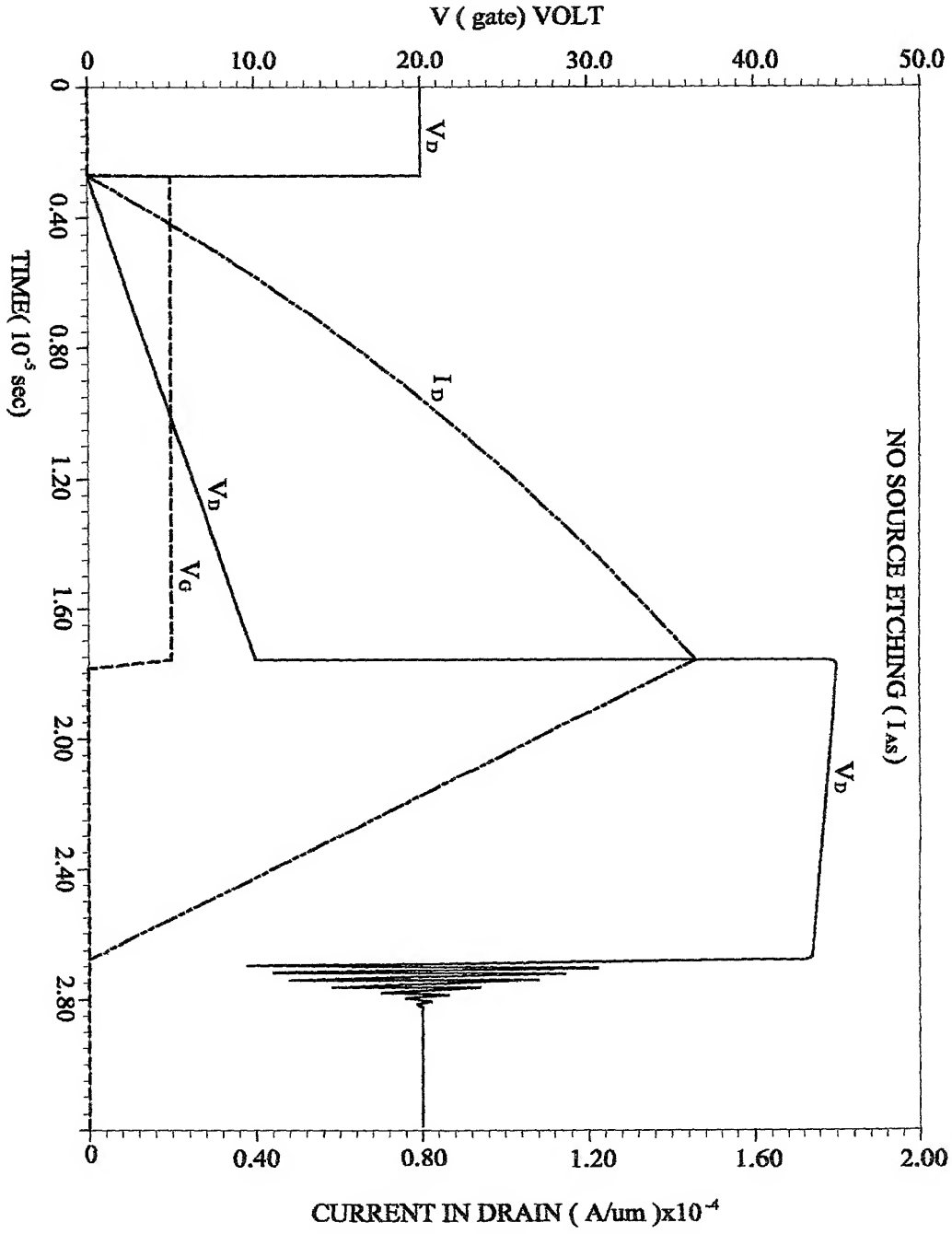


FIG. 8 (PRIOR ART)

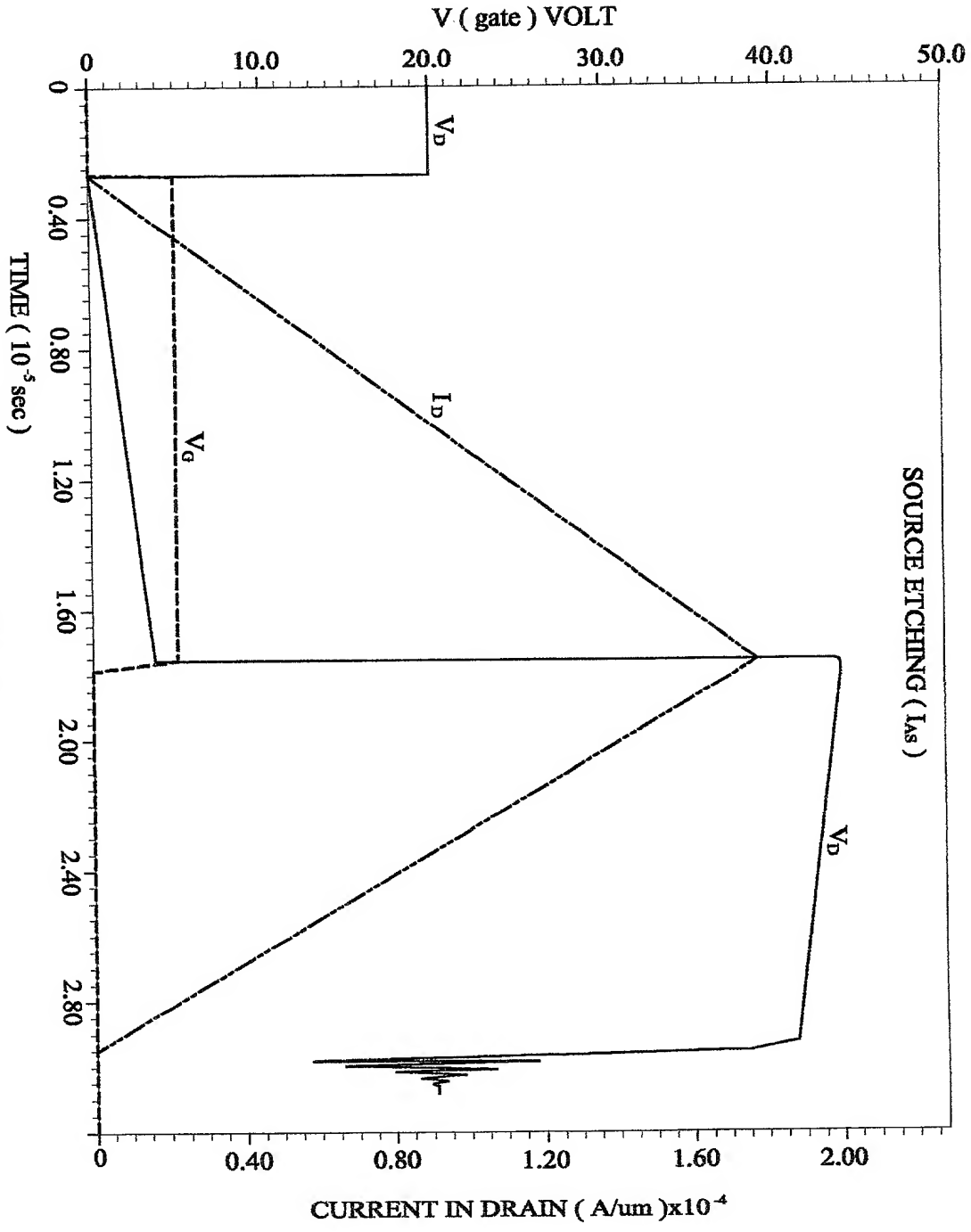

V (gate) VOLT
0
10.0
20.0
30.0
40.0
50.0
TIME (10⁻⁵ sec)
0
0.40
0.80
1.20
1.60
2.00
2.40
2.80
V_D
V_G
I_D
V_D
SOURCE ETCHING (I_AS)
0
0.40
0.80
1.20
1.60
2.00
CURRENT IN DRAIN (A/um) x10⁻⁴

FIG. 9